A Resilient Runtime Environment for HPC and Internet Core Router Systems

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SPRTG Projects

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A Multiple Institution Project

- Cisco Team
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  - **Timothy I. Mattox**
  - Robert M. Broberg
  - Jeffrey M. Squyres
- University Collaborators
  - Joshua Hursey, Indiana University
  - Chase Cotton, University of Delaware
  - Jonathan M. Smith, University of Pennsylvania
- Open MPI Project, http://www.open-mpi.org/

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HPC and Internet Core Router Systems

- Highly parallel with various processor interconnects
- Trends that lower the whole system MTBF
  - Systems are growing in size and complexity
  - Increasing demands for new features
- Different fault tolerance needs
  - HPC Systems need long uptimes to effectively run large parallel applications
  - Internet Core Routers need non-stop operation to not disrupt services
    - IP Telephony
    - Video Conferencing

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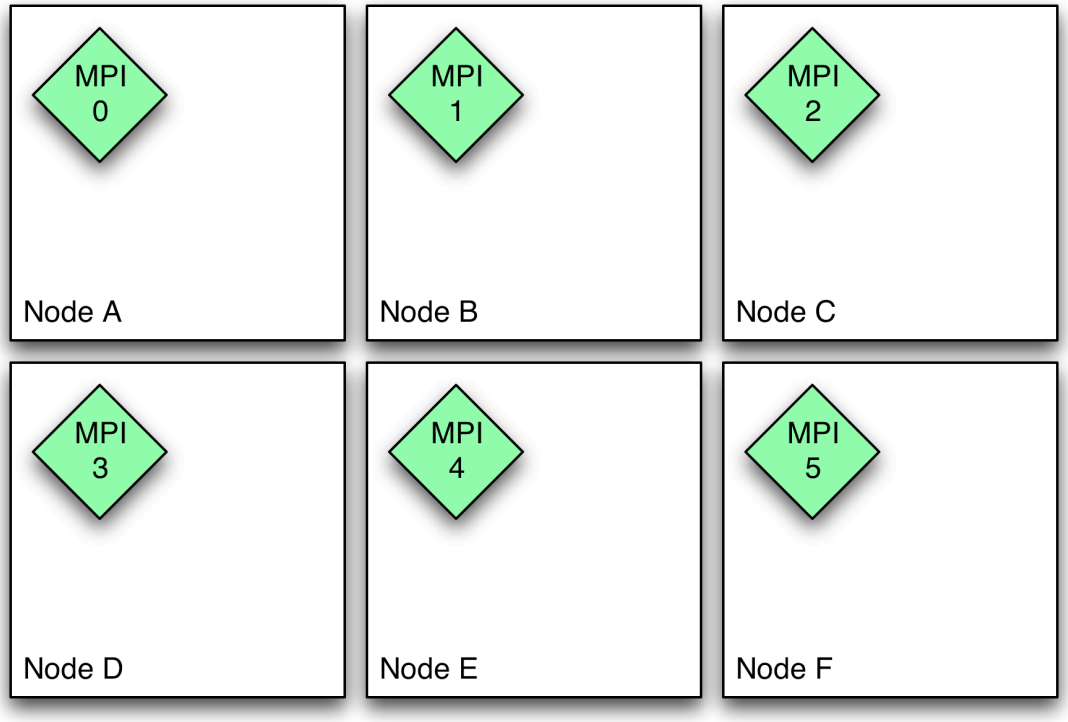
HPC System Architecture Slice



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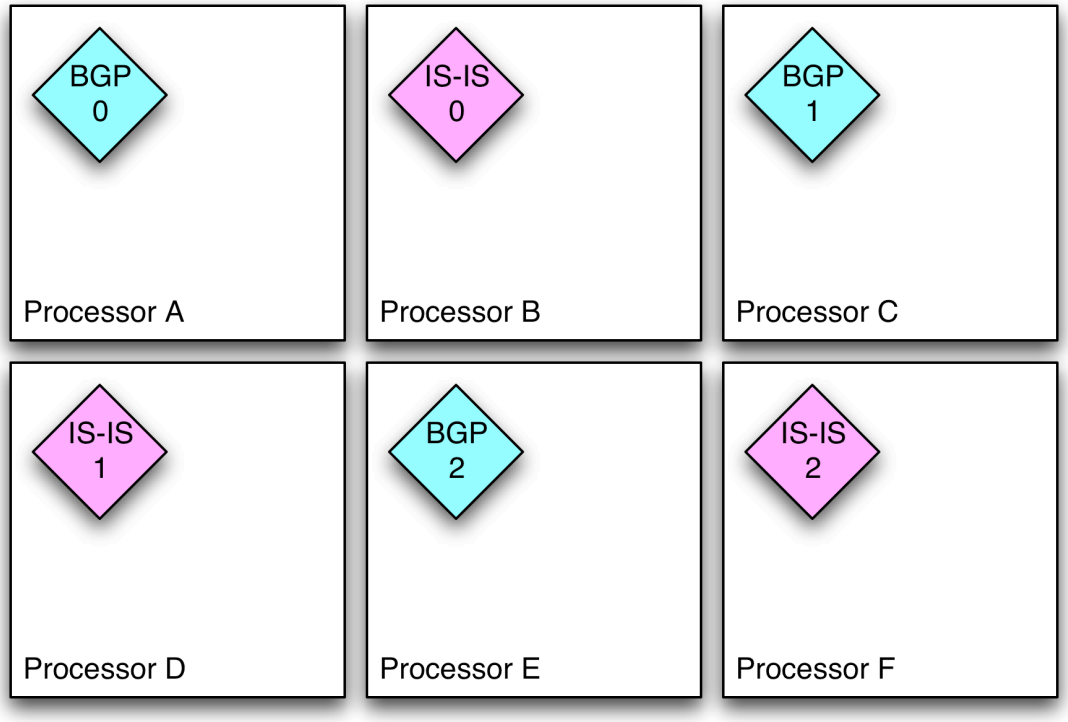
Internet Core Router Control Plane



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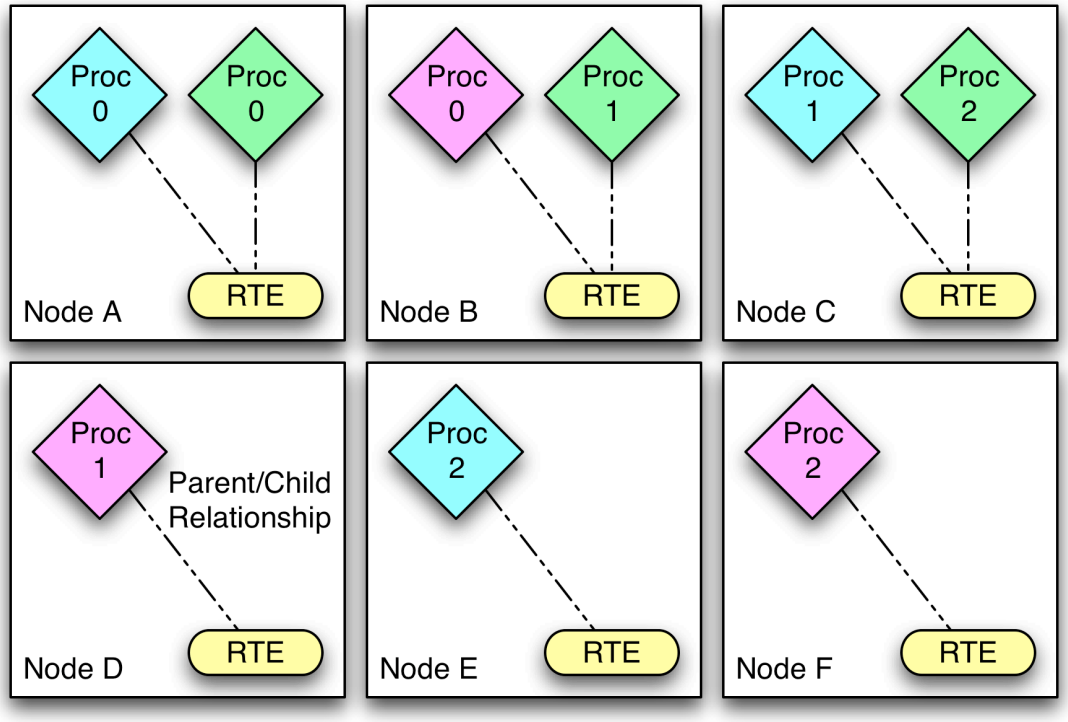
Common Infrastructure



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Open MPI's Runtime Environment (ORTE)

- Open Source (New BSD License)
  - 27 total Member, Partner, and Contributor organizations
- Modular Component Architecture (MCA)
  - Provides flexibility
  - Supports good software engineering practice

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A Resilient Runtime Environment Needs

- Fault Detection
- Fault Recovery
- Fault Prediction
- Fault Group Model

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Our Additions/Enhancements to ORTE

- Sensor Framework
- Recovery Service (RecoS) Framework
- Resilient Mapper Component
- ClusterManager Routed Component

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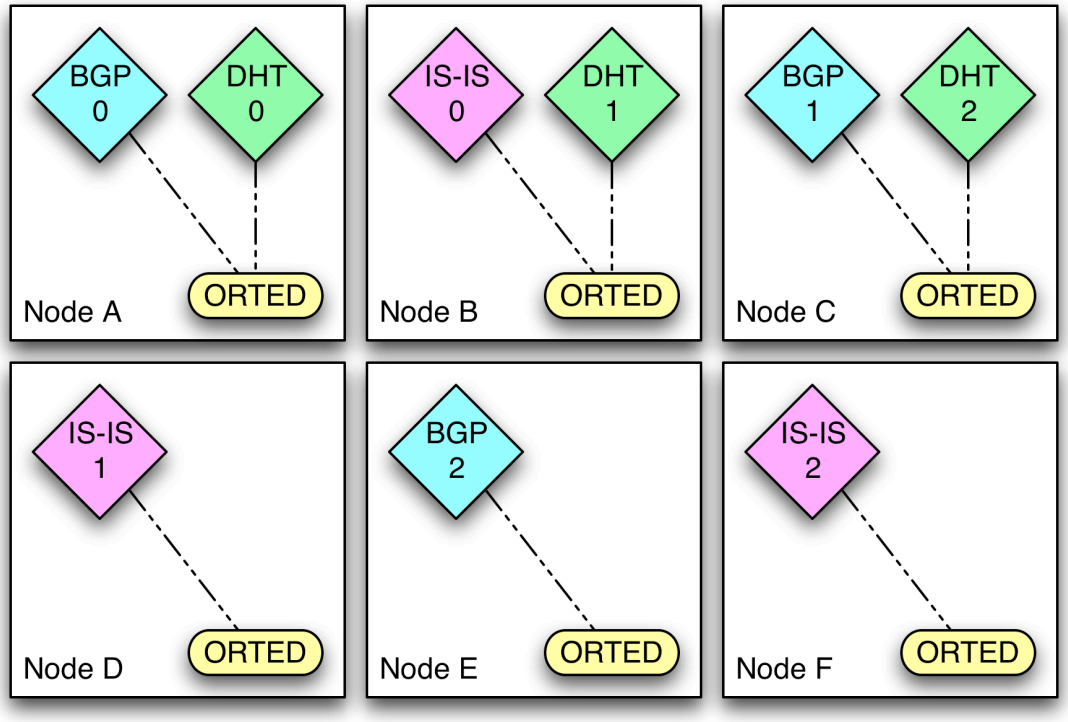
Example Fault Detection



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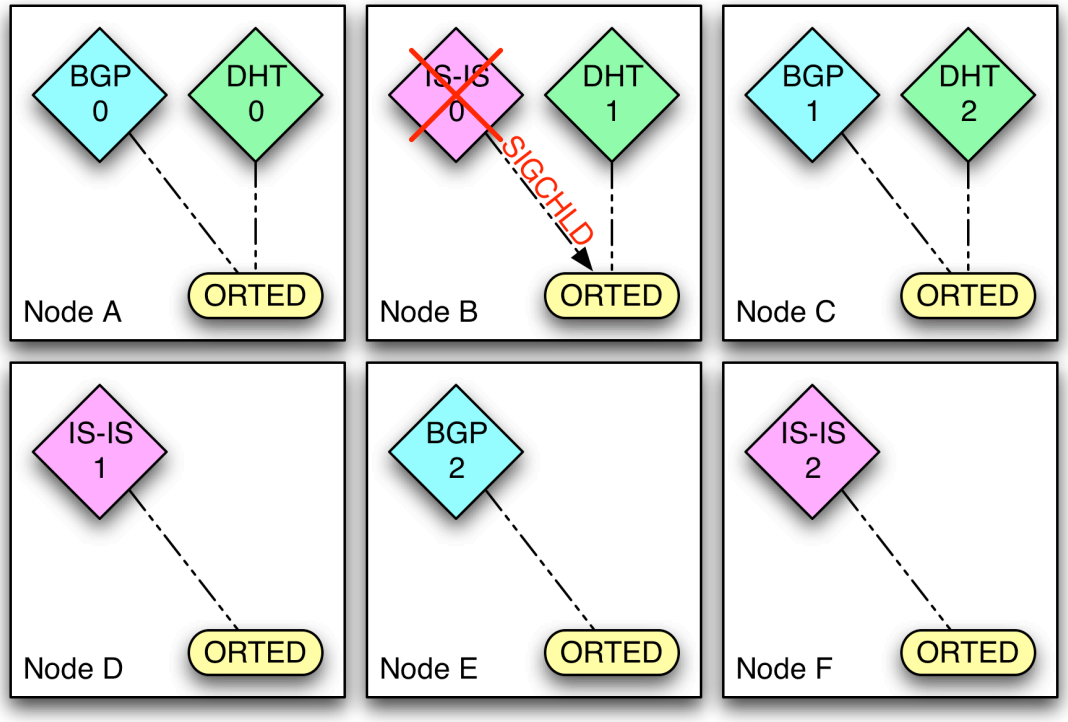
Example Fault Detection



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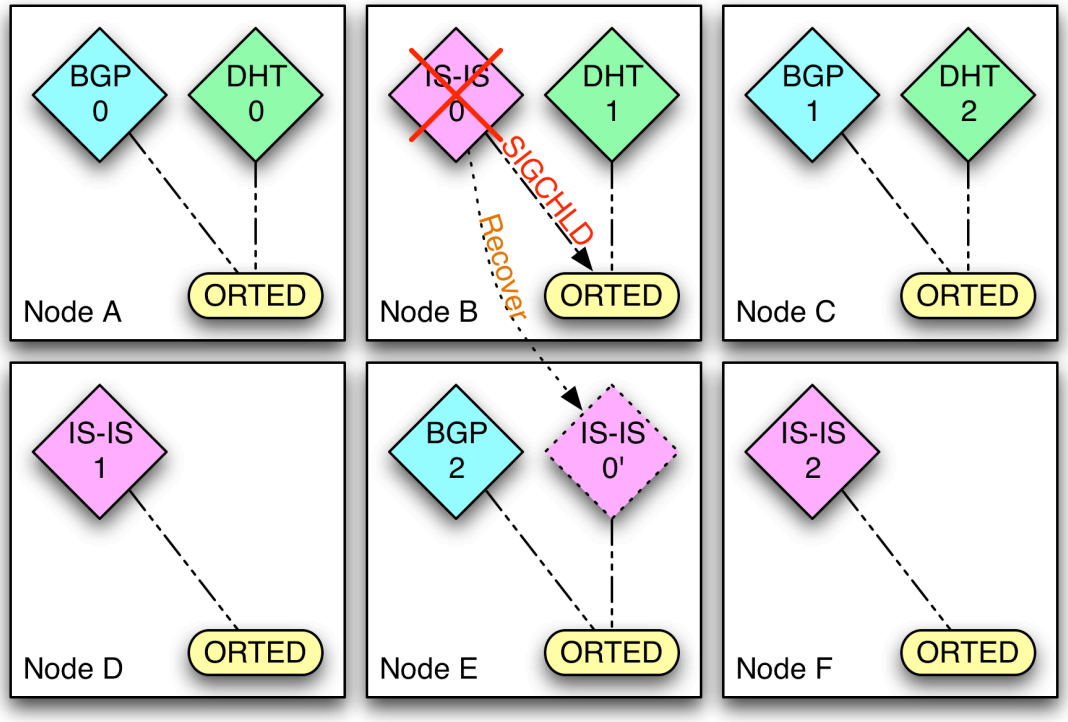
Example Fault Recovery



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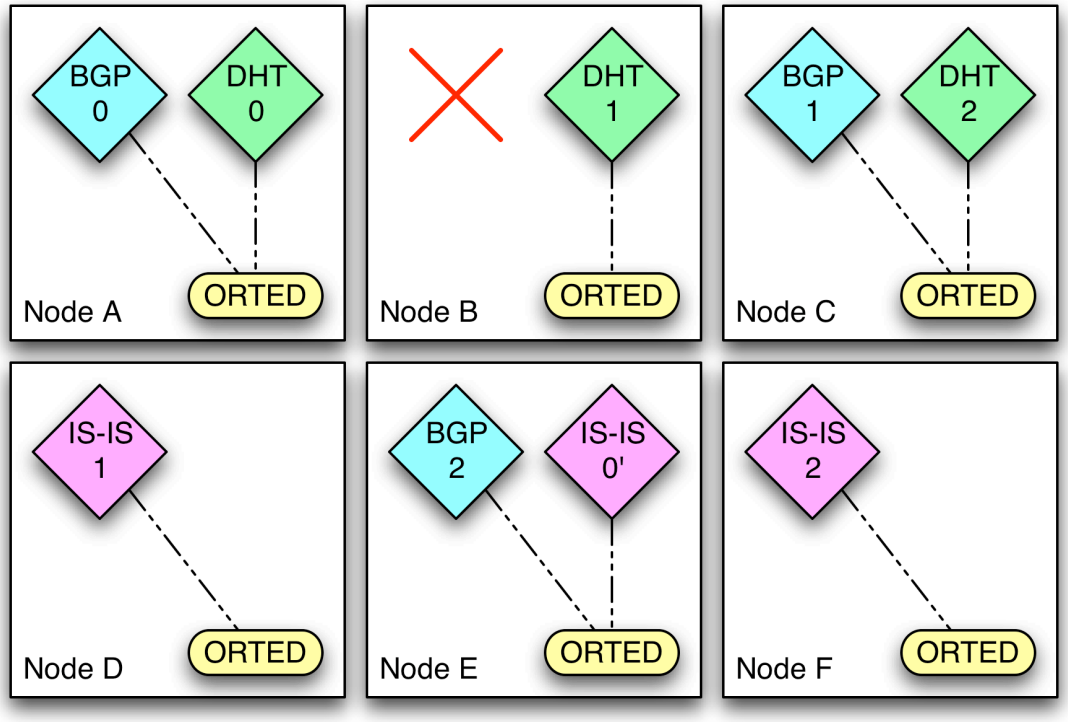
Example Fault Recovery



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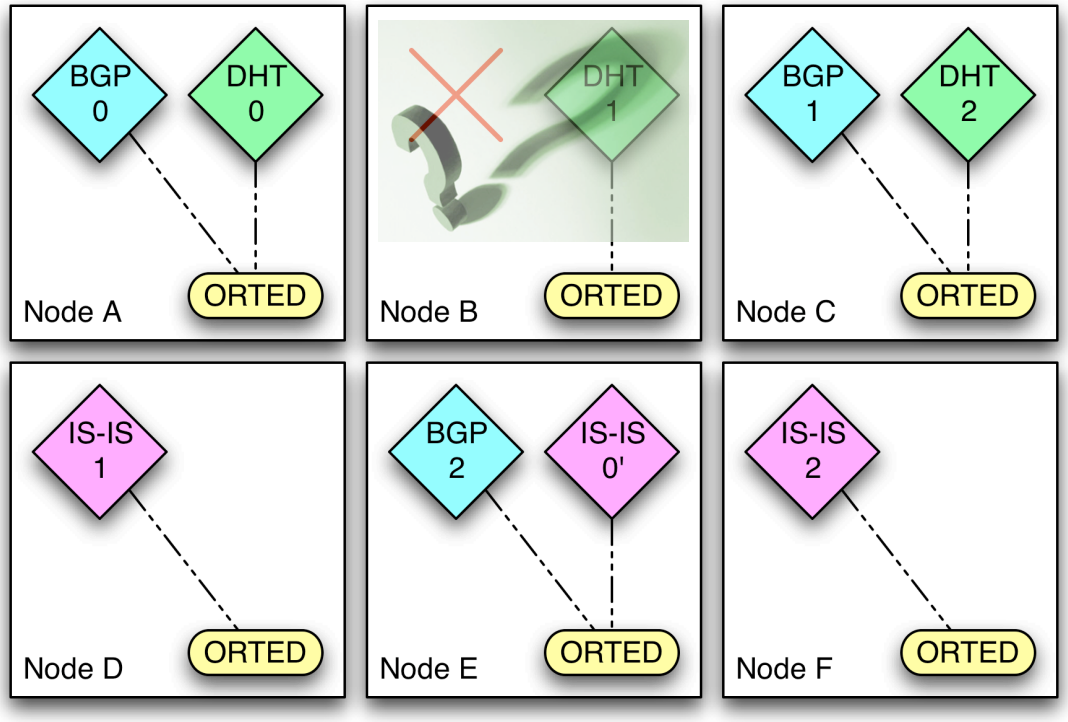
Example Fault Prediction



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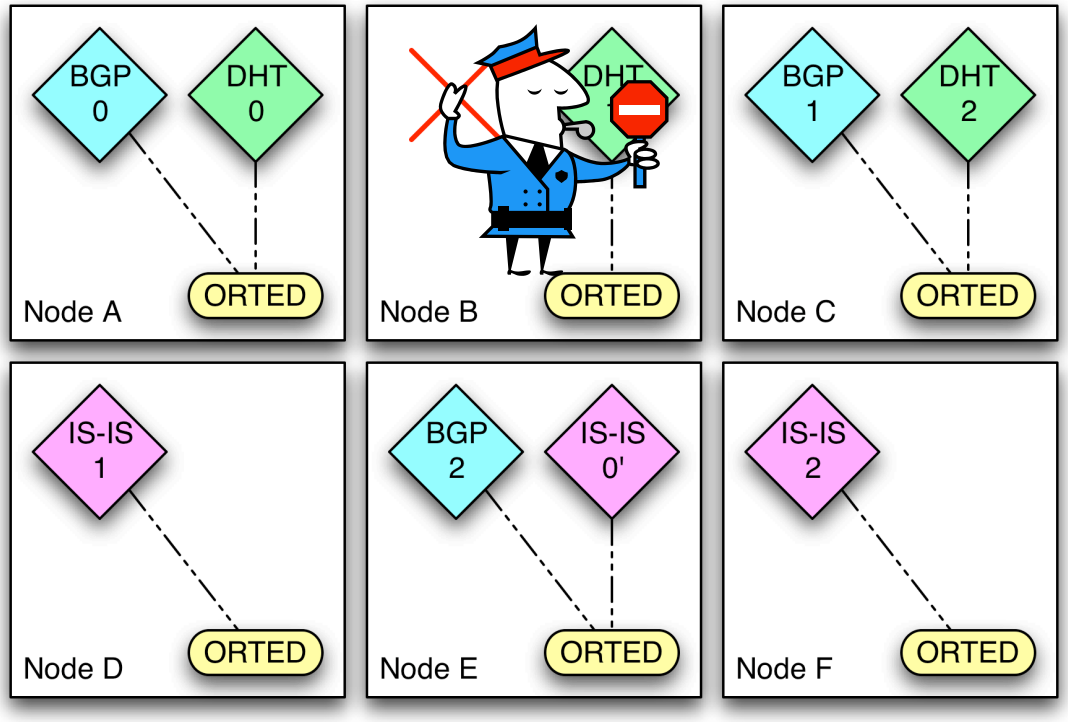
Example Fault Prediction



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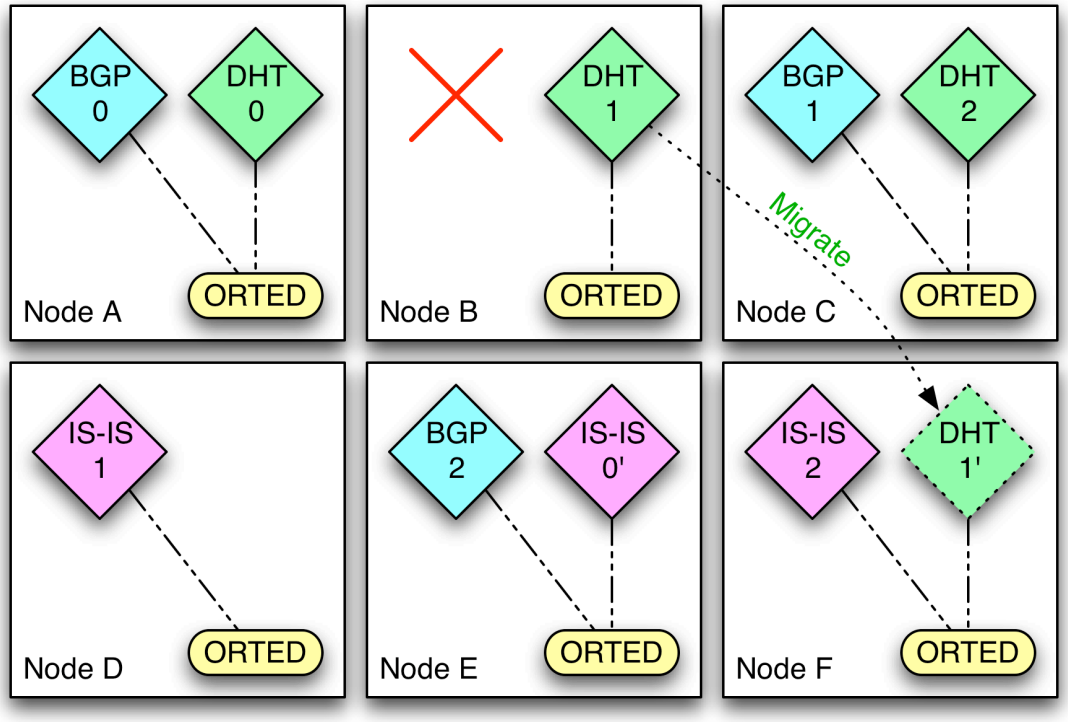

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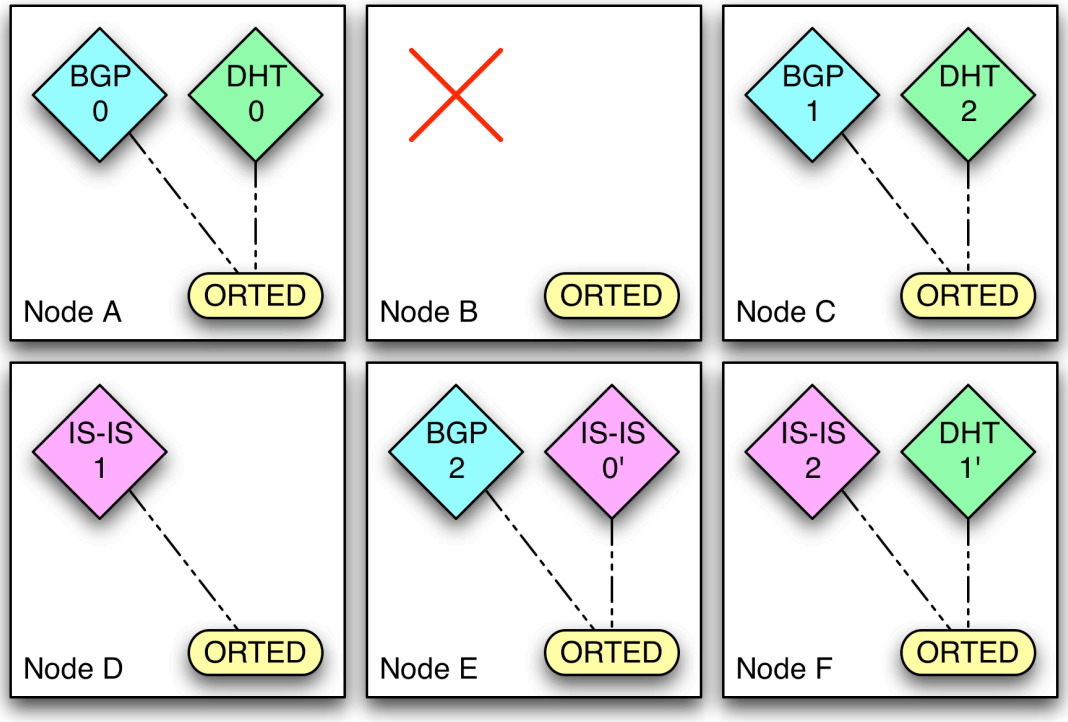

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Preliminary Results

- Non-MPI process restart in ~6 milliseconds
  - Local shell script takes ~3 milliseconds to start a process
  - Remote shell script takes ~80 milliseconds via ssh
- MPI process migration vs. checkpoint/restart
  - 128 process LAMMPS metallic solid benchmark
  - 6 GB of state distributed on 32 nodes
  - Factor of five reduction in overhead migrating 4 processes vs. checkpoint/restart

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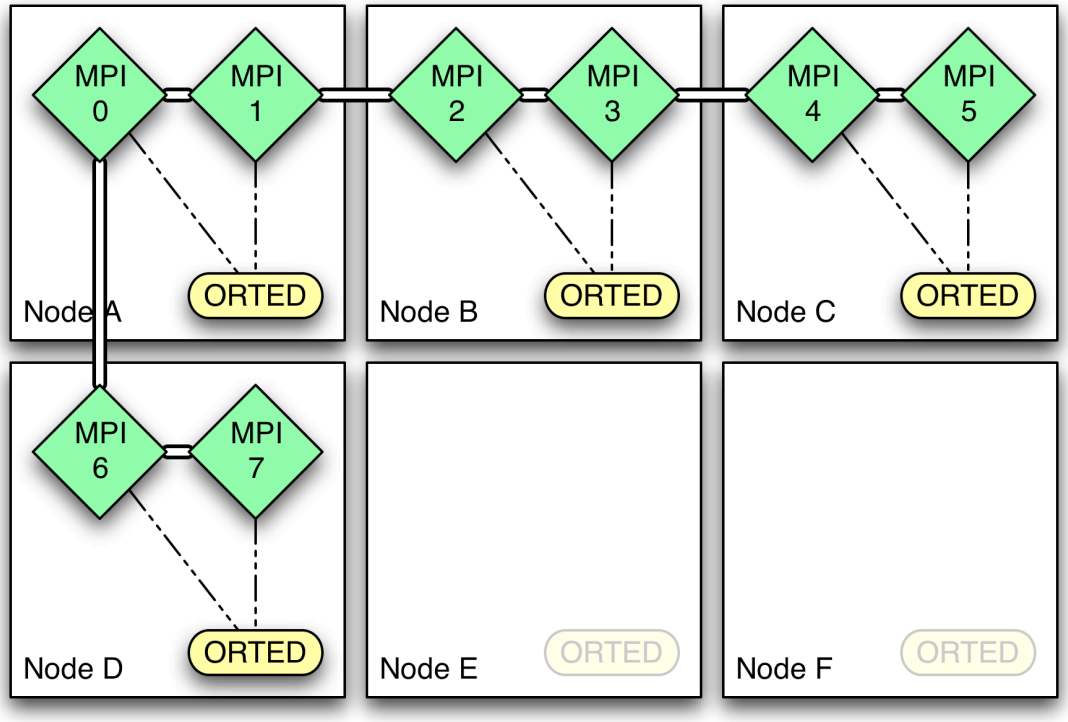
Example MPI Process Migration



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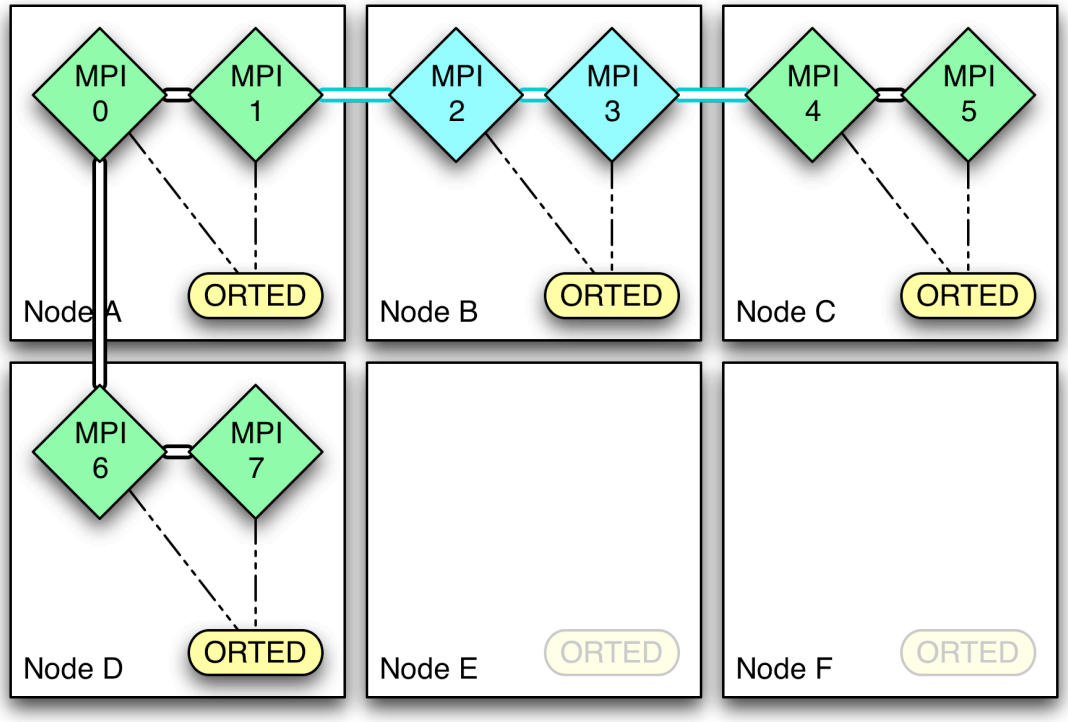

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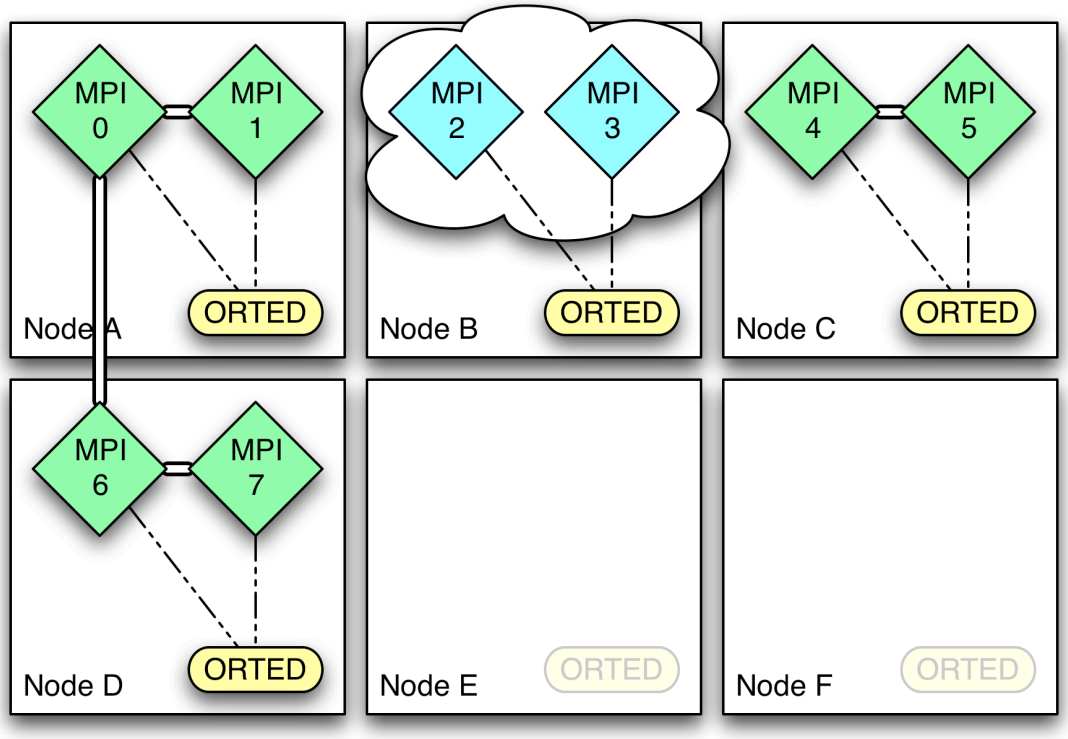
Example MPI Process Migration



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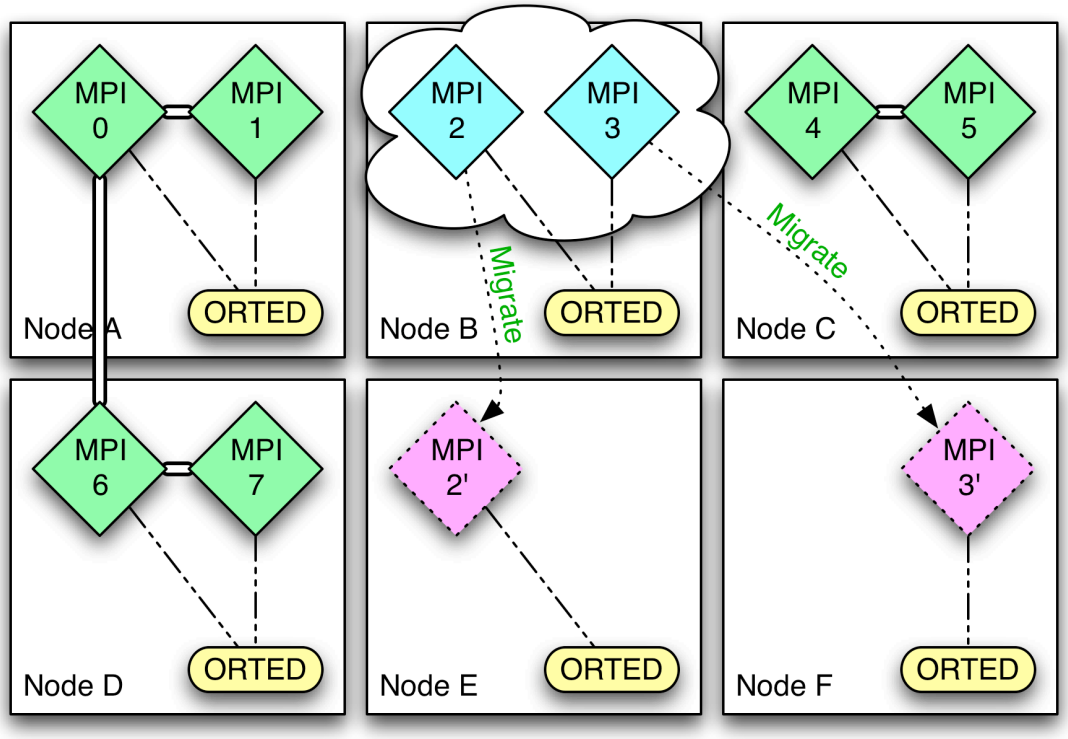
Example MPI Process Migration



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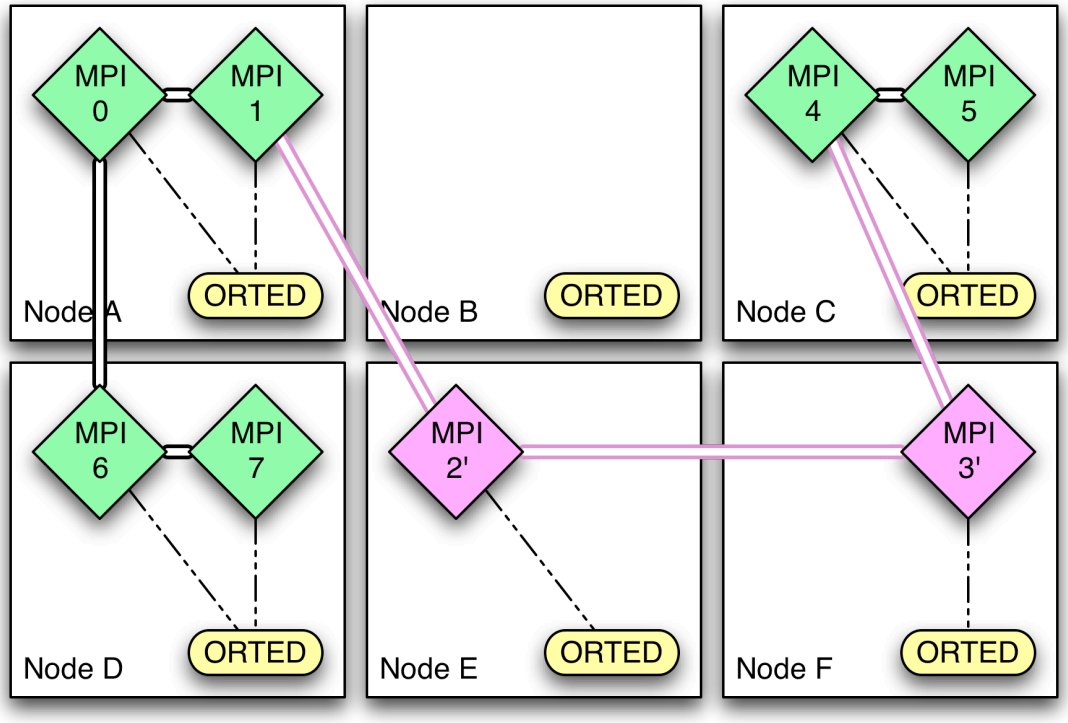

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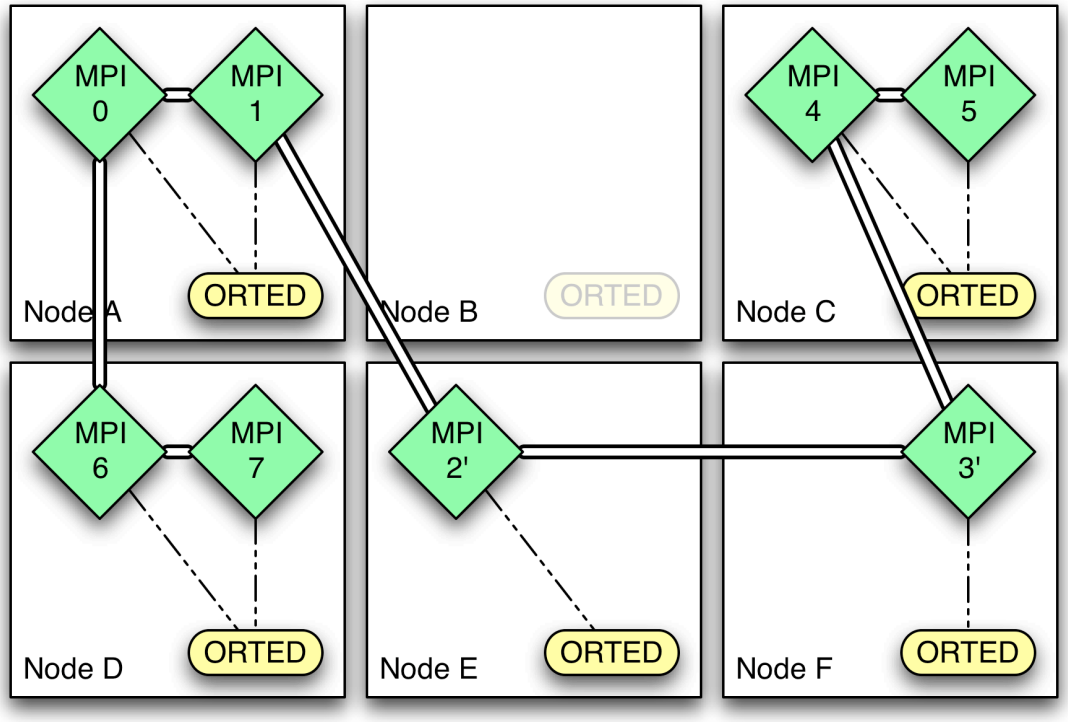
Example MPI Process Migration



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Some Planned Future Extensions

- More sensor components
- More and better fault prediction algorithms
- More fault detection techniques
- Interface with more external fault notification systems

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Conclusions

The overlap of goals for HPC and Internet Core Router System resiliency has resulted in a synergistic advancement in the Open MPI Runtime Environment software.

For more information:

See our poster (#47) in the Oregon Ballroom Lobby

Visit the Reliable Router Research (R3) website

http://r3.cis.upenn.edu/

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